

FIG. 1 is a block diagram of a computer system 10. The computer system 10 includes a processor 24, a memory 12, an input/output (I/O) device 26, and a reduced image 32. The memory 12 includes a reduction system 14, a pixel selection system 18, and an enhancement system 22. The reduction system 14 includes a reduction algorithm 16, which includes a rotation system A and a rotation system B. The pixel selection system 18 includes K values 20. The input/output (I/O) device 26 is connected to the processor 24 and the memory 12. The reduced image 32 is connected to the processor 24 and the memory 12.

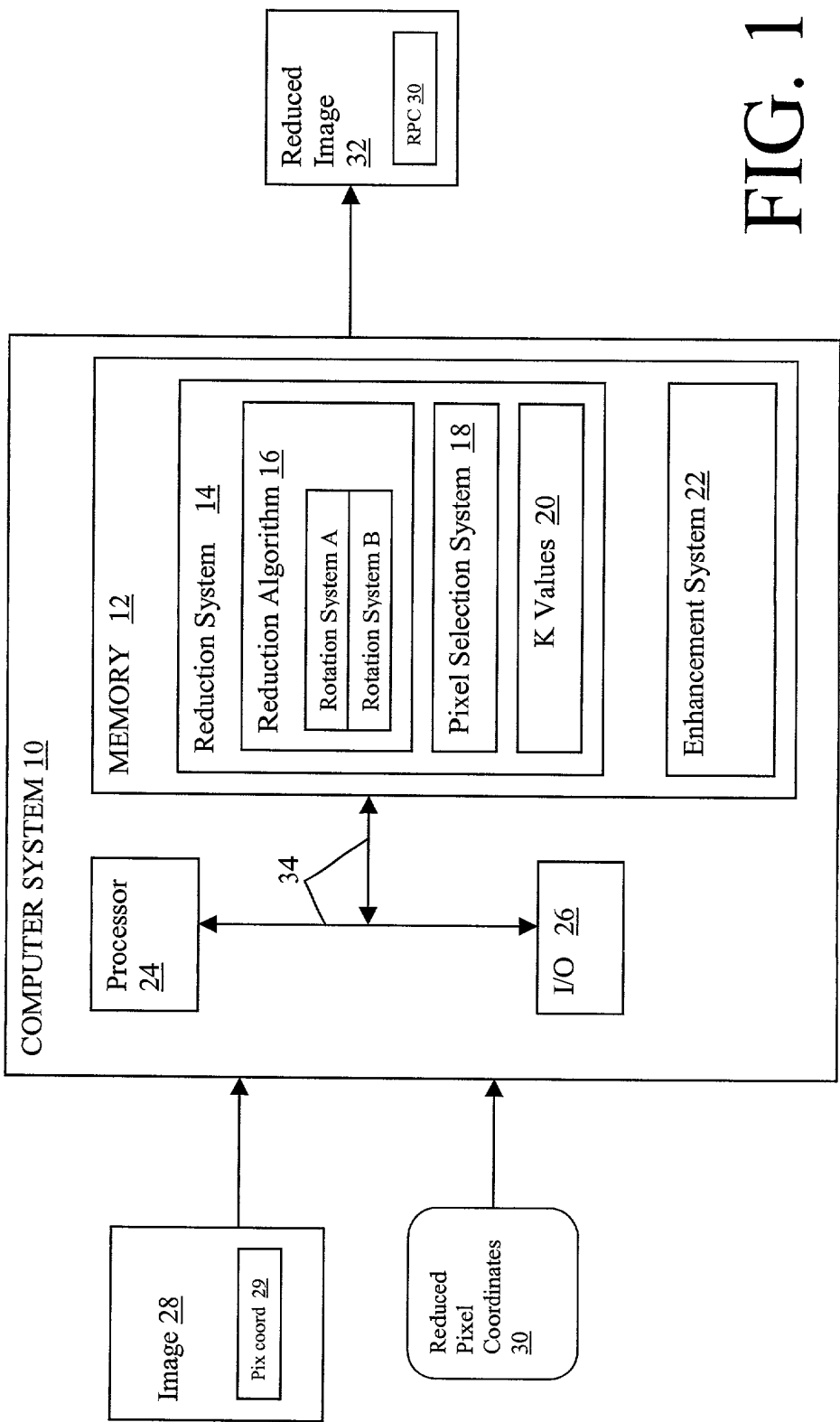


FIG. 1

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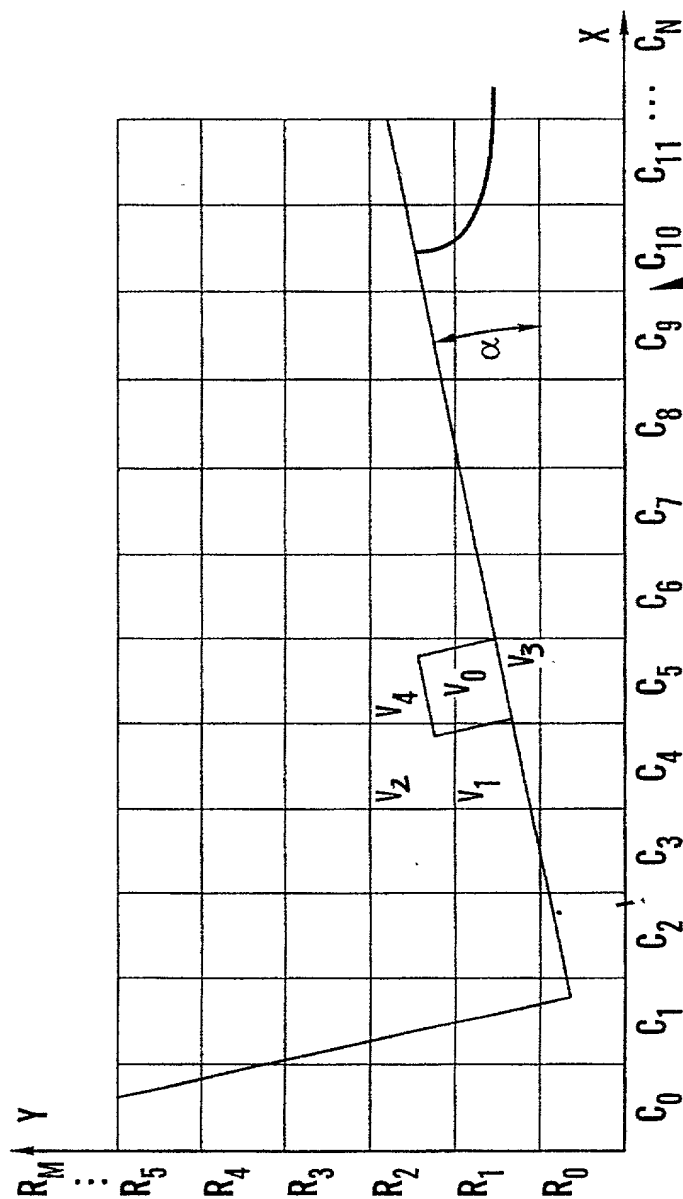


FIG. 2 42

FIG. 3

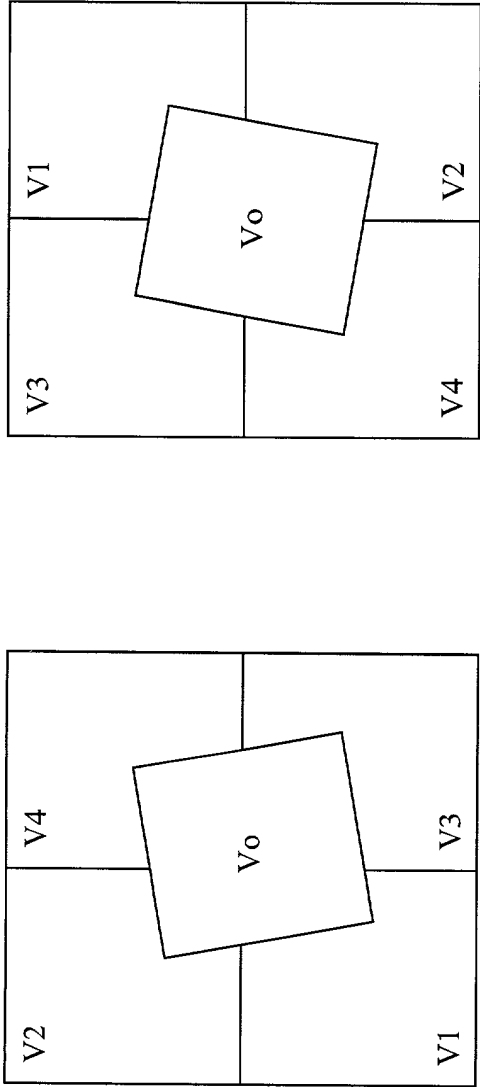


FIG. 3